Amendments to the Claims:

This listing of claims replaces all prior versions and listings of claims in the application:

Listing of Claims:

1-22. (Cancelled)

23.   (Original) A method comprising:

receiving a packet of encoded symbols;

jointly quantizing multiple symbols;

decoding the jointly quantized symbols to obtain a result; and

decompressing the result into individual decoded symbols.

24.   (Original) The method of claim 23 including decoding the jointly quantized symbols

using a look-up table that approximates output of an algorithmic decoding process.

25.   (Original) The method of claim 23 including decoding the jointly quantized symbols

using a look-up table that approximates output a soft-input soft-output algorithmic decoding

process, a soft-input hard-output algorithmic decoding process, a hard-input soft-output

algorithmic decoding process, or a hard-input hard-output algorithmic decoding process.

26.   (Original) An article comprising a computer-readable medium that stores computer-executable instruction for causing a computer system, in response to receiving a packet of encoded symbols, to:

jointly quantize multiple ones of the symbols;

decode the jointly quantized symbols to obtain a result; and

decompress the result into individual decoded symbols.

27.   (Original) The article of claim 26 including instructions for causing the computer system to decode the jointly quantized symbols using a look-up table that approximates output of an algorithmic decoding process.

28.   (Currently amended) The ~~method~~ article of claim 26 including instructions for causing the computer system to decode the jointly quantized symbols using a look-up table that approximates output of a soft-input soft-output algorithmic decoding process, a soft-input hard-output algorithmic decoding process, a hard-input soft-output algorithmic decoding process, or a hard-input hard-output algorithmic decoding process.

29.   (New) An apparatus comprising:

a memory storing a look-up table that approximates output of an algorithmic decoding process; and

a processor configured to:

jointly quantize symbols received in a packet of encoded symbols;

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decode the jointly quantized symbols to obtain a result; and

decompress the result into individual decoded symbols.

30. (New) The apparatus of claim 29 wherein the look-up table stores information that approximates output of a soft-input soft-output algorithmic decoding process

31. (New) The apparatus of claim 29 wherein the look-up table stores information that approximates output of a soft-input hard-output algorithmic decoding process.

32. (New) The apparatus of claim 29 wherein the look-up table stores information that approximates output of a hard-input soft-output algorithmic decoding process.

33. (New) The apparatus of claim 29 wherein the look-up table stores information that approximates output of a hard-input hard-output algorithmic decoding process.

Amendments to the Drawings:

The attached replacement sheets of formal drawings replace the original sheets of drawings.

Attachments following last page of this Amendment:

Replacement Sheets (4 pages)